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Examiner Vibol Tan
Patent & Trademark Office
Washington, DC 20231

On August 14, 2001

TOWNSEND and TOWNSEND and CREW LLP

By: Lata Olivier
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PATENT
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Andrew V. Podlesny et al.

Application No.: 09/505,656

Filed: June 27, 2000

For: HIGH-SPEED LOW-POWER
DATA TRANSFER SCHEME

Examiner: Vibol Tan

Art Unit: 2819

SUPPLEMENTAL AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to our telephone conversation of this morning attached is Supplemental
Amendment to Claim 1.

IN THE CLAIMS:

- 1 1. (TWICE AMENDED) A data transfer arrangement comprising:
- 2 two bus drivers;
- 3 a voltage precharge source;
- 4 a differential bus coupled to the bus drivers and to the voltage precharge
- 5 source; and
- 6 a latching sense amplifier coupled to the differential bus;
- 7 wherein the latching sense amplifier comprises:
- 8 a first stage including a cross-coupled latch coupled to a differential
- 9 data bus; and